**CALL FOR PAPERS**

**Special Issue on**

# High Performance Computing and Communication (HPCC 2015)

*Journal of Signal Processing Systems*

With the rapid growth in computing and communications technology, the past decade has witnessed a proliferation of powerful parallel and distributed systems and an ever-increasing demand for practice of ***High Performance Computing and Communications*** (HPCC). HPCC has moved into the mainstream of computing and has become a key technology in determining future research and development activities in many academic and industrial branches, especially when the solution of large and complex problems must cope with very tight timing schedules.

**Scope of Contributions**

The object of this special issue is to explore recent advances and disseminate state-of-the-art research on novel technologies on High Performance Computing and Communication. Submission of a manuscript implies: that the work described has not been published before; that it is not under consideration for publication anywhere else; that its publication has been approved by all co-authors, if any, as well as by the responsible authorities – tacitly or explicitly – at the institute where the work has been carried out. The topics of interest for the special issue include, but are not limited to:

1. High Performance Computing and Communication
   - Parallel and distributed system architectures
   - Languages and compilers for high performance computing
   - Parallel and distributed software technologies
   - Parallel and distributed algorithms
   - Embedded systems
   - Peer-to-peer computing
   - Grid and cluster computing
   - Web services and Internet computing
   - Cloud computing
   - Utility computing
   - Performance evaluation and measurement
   - Tools and environments for software development
2. Embedded Software and System
   - Embedded System Architecture
   - Embedded Software Architectures
   - Embedded OS, Scheduling and Runtime Support
   - Embedded Storage and I/O Systems
   - Real-Time Embedded Systems
   - Distributed and Networked Embedded Systems
   - Fault Tolerant and Trusted Embedded Systems

- Mixed-Critical Embedded Systems
- Heterogeneous SoC and Multicore Embedded Systems
- Power and Thermal Aware Computing
3. Cyberspace Safety and Security
   - Data and Applications Security
   - Network and Communications Security
   - Software and Systems Security
   - Cloud Security
   - Security and privacy issues in green cloud computing

## Manuscript Submission

Authors must follow the Journal of Signal Processing Systems guidelines for preparation of the manuscript. For further details, please refer to "Instruction for Authors" in Journal of Signal Processing Systems website at: http://www.springer.com/engineering/signals/journal/11265. Authors should submit their manuscripts online, please follow the instructions at website: http://www.editorialmanager.com/vlsi/. Please select "SI: HPCC". PDF is not an acceptable file format. Any submission that fails to follow the instructions will be rejected without review.

## Important Dates
Submission Deadline: November 1, 2015
Acceptance Notification: February 1, 2016
Final Manuscript Due: April 1, 2016
Publication: June 2016

## Guest Editors

Meikang Qiu (mqiu@pace.edu), Pace University, New York City, NY, USA
Peng Zhang (peng.zhang@stonybrook.edu), SUNY Stony Brook, NY, USA